Keysight Technologies

U2300A Series USB Modular Multifunction Data Acquisition Devices

Data Sheet









Introduction

The Keysight Technologies, Inc. U2300A Series USB modular multifunction data-acquisition (DAQ) devices offer a high-performance PC data-acquisition solution. The U2300A Series DAQ devices consist of two families: the basic multifunction DAQ comes in four models while the high density multifunction DAQ is made up of three models. The U2300A Series DAQ devices applications extend across industrial and education environments. Extending across industrial and education environments, the U2300A series DAQ with fast sampling rates is well suited for research and development, manufacturing as well as design validation designers.

Features

- Up to 3 MSa/s sampling rate for a single channel
- Functions as a standalone or modular unit
- Easy to use: Plug-and-play and hot swappable with Hi-Speed USB 2.0
- Up to 384 channels when incorporated into U2781A Keysight modular product chassis
- Easy-to-use bundled software for quick setup and data logging to PC
- 12-bit or 16-bit analog-to-digital (A/D) resolution
- 24-bit programmable digital input/output
- Self-calibration capability
- Compatible with a wide range of Application Development Environments
- USB 2.0 and USBTMC-USB488 standards



High sampling rate

The U2300A Series DAQ devices can generate sampling rates of up to 3 MSa/s for a single channel. When multiple channels are configured, the device can sample data up to 1 MSa/s. The fast sampling capability allows users to perform intermittent detection easily. This is ideal when dealing with high density analog input/output signals especially when juggling between different input ranges and sampling requirements.

Flexible standalone or modular capability

The U2300A Series DAQ devices are uniquely designed with the flexibility to function as a standalone unit or as a part of a modular unit. When used together with the U2781A modular product chassis, the devices has the capability to support up to 384 channels.

Flexible system and control options with polling and continuous mode

The U2300A Series DAQ devices have two modes, polling mode and continuous mode. Selecting continuous mode enables you to acquire data continuously once the trigger signal is received.

Arbitrary waveform

Designed to support arbitrary waveforms, the U2300A Series allows you to generate arbitrary waveform via the Keysight Measurement Manager application software or SCPI commands.

Burst mode

Equipped with the burst mode, the enhancement feature enables simultaneous mode for analog input acquisition. Now can you can perform sampling measurement up to the highest possible speed of the DAQ.

Trigger sources

U2300A Series offers various trigger options from immediate trigger (none), analog/external digital trigger, System Synchronous Interface (SSI)/ Star trigger and Master/Slave trigger sources. These entire trigger options has the capability to configure trigger sources during A/D and digital-to-analog (D/A) operations. Selecting the slave trigger and SSI/Star trigger are recommended when the USB modules are used together with the Keysight U2781A USB modular product chassis.

Predefined function generator

Aside from supplying DC voltage, the two analog output channels are capable of generating common and predefined waveforms such as sinusoidal wave, square wave, triangle wave, sawtooth wave and noise wave.



Product outlook and dimensions

Front view



Rear view



Top view



Standard shipped accessories

- AC/DC Power adapter
- Power cord
- USB extension cable
- L-Mount kit (used with modular product chassis)
- Keysight USB Modular Products Quick Start Guide
- Keysight USB Modular Products Reference CD-ROM
- Keysight Automation-Ready CD-ROM (contains the Keysight IO Libraries Suite)
- Certificate of Calibration

Optional accessories

- U2901A Terminal block and SCSI-II 68pin connector with 1-meter cable
- U2902A Terminal block and SCSI-II 68pin connector with 2-meter cable

Product characteristics and general specifications

Remote interface

- Hi-Speed USB 2.0
- USBTMC-USB4881

Power requirement

- +12 VDC (TYPICAL)
- 2 A (MAX) input rated current

Power consumption

+12 VDC, 550 mA maximum

Operating environment

- Operating temperature from 0 °C to +55 °C
- Relative humidity at 15% to 85% RH (non-condensing)
- Altitude up to 2000 meters
- Pollution Degree 2
- For indoor use only

Storage compliance

-20 °C to 70 °C

Safety compliance

Certified with:

- IEC 61010-1:2001/EN 61010-1:2001 (2nd Edition)
- USA: UL61010-1: 2004
- Canada: CSA C22.2 No.61010-1:2004

EMC compliance

- IEC/EN 61326-1 1998
- CISPR 11: 1990/EN55011:1991, Class A, Group 1
- Canada: ICES-001: 1998
- Australia/New Zealand: AS/NZS 2064.1

Shock and vibration

Tested to IEC/EN 60068-2

10 connector

68-pin female VHDCI Type

Dimension (W \times D \times H)

Module dimension:

- 120.00 mm × 182.40 mm × 44.00 mm (with plastic casing)
- 105.00 mm × 174.54 mm × 25.00 mm (without plastic casing)

Terminal block dimension:

- 103.00 mm × 85.20 mm × 42.96 mm

Weight

- 565 g (with plastic casing)
- 400 g (without plastic casing)

Warranty

Three years for U2300A series DAQ devices

Three months for standard shipped accessories



¹ Compatible with Microsoft Windows operating systems only. Requires a direct USB connection to the PC so the appropriate driver can be installed in the USB DAQ module.

Electrical specifications

Basic multifunction USB DAQ

Model number	U2351A	U2352A	U2353A	U2354A		
Analog input						
Resolution		16 bits, no	missing codes			
Number of channels		16 SE/8 DI (software selectable/channel)				
Maximum sampling rate ¹	250 kSa/s 500 kSa/s					
Scan list memory		Up to 100 selec	table channel entries			
Programmable bipolar input range		±10 V, ±5 V	, ±2.5 V, ±1.25 V			
Programmable unipolar input range		0 to 10 V, 0 to 5 V	, 0 to 2.5 V, 0 to 1.25 V			
Input coupling			DC			
Input impedance		1 GΩ	1 / 100 pF			
Operational common mode voltage range		±7.	5 V _{maximum}			
Overvoltage protection	Power-o	n: Continuous ±30	V, Power-off: Continuous ±15	V		
Trigger sources	-		al trigger, SSI/Star trigger ²			
Trigger modes			post-trigger, and middle-trigge	ır		
FIFO buffer size			to 8 MSa			
Analog output		·				
Resolution	16 bits	-	16 bits	_		
Number of channels	2	-	2	-		
Maximum update rate	1 MSa/s	-	1 MSa/s	-		
Output ranges	0 to 10 V, ±10 V, 0 to AO_EXT_REF, ±AO_EXT_REF ³	-	0 to 10 V, ±10 V, 0 to AO_EXT_REF, ±AO_EXT_REF ³	-		
Output coupling	DC	-	DC	-		
Output impedance	0.1 Ω typical	-	0.1 Ω typical	-		
Stability	Any passive load up to 1500 pF	-	Any passive load up to 1500 pF	-		
Power-on state	0 V steady state	-	0 V steady state	-		
Trigger sources	External analog/digital trigger, SSI/Star trigger²	-	External analog/digital trigger, SSI/Star trigger ²	-		
Trigger modes	Post-trigger and delay-trigger	-	Post-trigger and delay-trigger	-		
FIFO buffer size	One channel: Maximum 8 MSa Two channels: Maximum 4 MSa/ch	-	One channel: Maximum 8 MSa Two channels: Maximum 4 MSa/ch	-		
Function generation mode	Sine, square, triangle, sawtooth, and noise waveforms	-	Sine, square, triangle, sawtooth, and noise waveforms	-		

Basic multifunction USB DAQ (continued)

Digital I/O	
Number of channels	24-bit programmable input/output
Compatibility	TTL
Input voltage	$V_{_{ L}}=0.7~V$ max, $I_{_{ L}}=10~\mu A$ max $V_{_{ H}}=2.0~V$ min, $I_{_{ H}}=10~\mu A$ max
Input voltage range	-0.5 V to +5.5 V
Output voltage	$V_{OL} = 0.45 \text{ V max}, I_{OL} = 8 \text{ mA max}$ $V_{OH} = 2.4 \text{ V min}, I_{OH} = 400 \mu\text{A max}$
General purpose digital counter	
Maximum count	(2 ³¹ –1) bits
Number of channels	Two independent up/down counter
Compatibility	TTL
Clock source	Internal or external
Base clock available	48 MHz
Maximum clock source frequency	12 MHz
Input frequency range⁴	0.1 Hz to 6 MHz at 50% duty cycle
Pulse width measurement range	0.167 µs to 178.956 s
Analog trigger	
Trigger source	All analog input channels, External analog trigger (EXTA_TRIG)
Trigger level	±Full scale for internal; ±10 V for external
Trigger conditions	Above high, below low, and window (software selectable)
Trigger level resolution	8 bits
Bandwidth	400 kHz
Input impedance for EXTA_TRIG	20 kΩ
Coupling	DC
Overvoltage protection	Continuous for ± 35 V _{maximum}
Digital trigger	
Compatibility	TTL/CM0S
Response	Rising or falling edge
Pulse width	20 ns minimum
Calibration ⁵	
On board reference voltage	5 V
Temperature drift	±2 ppm/°C
Stability	±6 ppm/1000 hrs
General	
Remote interface	Hi-Speed USB 2.0
Device class	USBTMC-USB488
Programmable interface	Standard Commands for Programmable Instruments (SCPI) and IVI-COM



¹ When multiple channels are used, the sampling rate of each channel is the maximum sampling rate divided by the number of channels used.

² System Synchronous Interface (SSI) and Star trigger commands are used when modular devices are used in the product chassis.

 $^{^3}$ Maximum external reference voltage for analog output channels (AO_EXT_REF) is ± 10 V.

⁴ Measurement frequency's resolution:

^{= 12} MHz/n, n = 2, 3, 4, 5, ..., 120 M

^{= 6} MHz, 4 MHz, 3 MHz, 2.4 MHz, 2.0 MHz, ..., 0.1 Hz (up to six decimal points)

 $^{^{\}rm 5}$ $\,$ 20 minutes warm-up time is recommended.

High density multifunction USB DAQ

Analog input Resolution 16 bits, no missing codes 12 bits, no missing codes Number of channels 64 SE/32 DI (software selectable/channel) Maximum sampling rate¹ 250 kSa/s 500 kSa/s 3 MSa/s (single channel) in MSa/s (multiple channels) Scan list memory Up to 100 selectable channel entries Programmable bipolar input range ±10 V, ±5 V, ±2.5 V, ±1.25 V ±1.25 V, ±1 V, ±0.5 V, ±0.25 V, ±0.05 V Programmable unipolar input range ±0 to 10 V, 0 to 5 V, 0 to 2.5 V, 0 to 1.25 V 0 to 10 V, 0 to 5 V, 0 to 4 V, 0 to 2.5 V, 0 to 1.25 V 0 to 10 V, 0 to 5 V, 0 to 4 V, 0 to 2.5 V, 0 to 1.25 V 0 to 10 V, 0 to 5 V, 0 to 4 V, 0 to 2.5 V, 0 to 1.25 V 0 to 10 V, 0 to 5 V, 0 to 4 V, 0 to 2.5 V, 0 to 1.25 V 0 to 10 V, 0 to 5 V, 0 to 4 V, 0 to 2.5 V, 0 to 1.25 V 0 to 10 V, 0 to 5 V, 0 to 4 V, 0 to 2.5 V, 0 to 1.25 V 0 to 10 V, 0 to 5 V, 0 to 4 V, 0 to 4 V, 0 to 2.5 V, 0 to 1.25 V 0 to 10 V, 0 to 4 V,	Model number	U2355A	U2356A	U2331A	
Number of channels	Analog input				
Maximum sampling rate 250 kSa/s 500 kSa/s 3 MSa/s (single channel) 1 MSa/s (multiple channels)	Resolution	16 bits, no	12 bits, no missing codes		
Name	Number of channels	64 SE/32 DI (software selectable/channel)			
Programmable bipolar input range	Maximum sampling rate ¹	250 kSa/s	500 kSa/s		
Programmable bipolar input range ±10 V, ±5 V, ±2.5 V, ±1.25 V ±1.25 V, ±1 V, ±0.5 V, ±0.25 V, 0 to 4 V, 0 to 2.5 V, 0 to 1 V, 0 to 5 V, 0 to 4 V, 0 to 0.5 V, 0 to 0.4 V, 0 to 0.5 V, 0 to 0.4 V, 0 to 0.5 V, 0 to 0.0 V, 0 to 1 V, 0 to 0.5 V, 0 to 0.0 V, 0 to	Scan list memory	U	p to 100 selectable channel e	entries	
Programmable unipolar input range 0 to 10 V, 0 to 5 V, 0 to 1.25 V, 0 to 1.25 V, 0 to 2.5 V, 0 to 2.0 V, 0 to 0.4 V, 0 to 0.1 V, 0 to 0.5 V, 0 to 0.4 V, 0 to 0.1 V, 0 to 0.5 V, 0 to 0.4 V, 0 to 0.1 V, 0 to 0.5 V, 0 to 0.4 V, 0 to 0.5 V, 0 to 0.4 V, 0 to 0.1 V, 0 to 0.5 V, 0 to 0.4 V, 0 to 0.1 V, 0 to 0.5 V, 0 to 0.4 V, 0 to 0.1 V, 0 to 0.5 V, 0 to 0.4 V, 0 to 0.1 V, 0 to 0.5 V, 0 to 0.4 V, 0 to 0.1 V, 0 to 0.5 V, 0 to 0.4 V, 0 to 0.1 V, 0 to 0.5 V, 0 to 0.4 V, 0 to 0.1 V, 0 to 0.5 V, 0 to 0.4 V, 0 to 0.1 V, 0 to 0.5 V, 0 to 0.4 V, 0 to 0.1 V, 0 to 0.5 V, 0 to 0.4 V, 0 to 0.1 V, 0 to	Programmable bipolar input range	±10 V, ±5 V,	±2.5 V, ±1.25 V	±1.25 V, ±1 V, ±0.5 V,	
Input impedance Operational common mode voltage range Overvoltage protection Power-on: Continuous ±30 V, Power-off: Continuous ±15 V Trigger sources External analog/digital trigger, SSI/Star trigger² Trigger modes Pre- trigger, delay-trigger, post-trigger, and middle-trigger FIFO buffer size Output ranges Output ranges Output ranges Output impedance Output impedance Stability Any passive load up to 1500 pF Power-on state Output ranges External analog/digital trigger, SSI/Star trigger² Trigger modes Trigger modes Trigger nodes External analog/digital trigger, SSI/Star trigger? Output simpedance Output impedance Output impedance Output impedance Output impedance Output impedance Output impedance Stability Any passive load up to 1500 pF Power-on state Output state Trigger sources External analog/digital trigger, SSI/Star trigger² Trigger modes Post-trigger and delay-trigger FIFO buffer size One channel: Maximum 8 MSa Two channels: Maximum 8 MSa Two channels: Maximum 4 MSa/ch Function generation mode Sine, square, triangle, sawtooth, and noise waveforms Digital I/O Number of bits Output voltage V _n = 0.7 V max, I _n = 10 μA max V _n = 2.0 V min, I _n = 10 μA max V _n = 2.0 V min, I _n = 10 μA max V _n = 0.5 V to +5.5 V Output voltage range V _n = 0.45 V max, I _n = 8 mA max	Programmable unipolar input range	0 to 10 V, 0 to 5 V,	0 to 2.5 V, 0 to 1.25 V	0 to 2.5 V, 0 to 2 V, 0 to 1 V, 0 to 0.5 V, 0 to 0.4 V, 0 to	
Operational common mode voltage range ±7.5 V maximum Overvoltage protection Power-on: Continuous ±30 V, Power-off: Continuous ±15 V Trigger sources External analog/digital trigger, SSI/Star trigger² Trigger modes Pre- trigger, delay-trigger, post-trigger, and middle-trigger FIFO buffer size Up to 8 MSa Analog output 12 bits Resolution 12 bits Number of channels 2 Maximum update rate 1 MSa/s Output ranges 0 to 10 V, ±10 V, 0 to AQ_EXT_REF, ±AQ_EXT_REF³ Output coupling DC Output impedance 0.1 Ω Typical Stability Any passive load up to 1500 pF Power-on state 0 V steady state Trigger sources External analog/digital trigger, SSI/Star trigger² Trigger modes Post-trigger and delay-trigger FIFO buffer size One channel: Maximum 8 MSa Two channels: Maximum 4 MSa/ch Function generation mode Sine, square, triangle, sawtooth, and noise waveforms Digital I/O Vulley to the programmable input/output Compatibility TL Input voltage Vulley to the programmable input/output Up to the program	Input coupling		DC		
Overvoltage protection Power-on: Continuous ±30 V, Power-off: Continuous ±15 V Trigger sources External analog/digital trigger, SSI/Star trigger² Trigger modes Pre- trigger, delay-trigger, post-trigger, and middle-trigger FIFO buffer size Up to 8 MSa Analog output In Sa/s Number of channels 2 Maximum update rate 1 MSa/s Output ranges 0 to 10 V, ±10 V, 0 to A0_EXT_REF, ±A0_EXT_REF³ Output coupling DC Output impedance 0.1 Ω Typical Stability Any passive load up to 1500 pF Power-on state 0 V steady state Trigger sources External analog/digital trigger, SSI/Star trigger² Trigger modes Post-trigger and delay-trigger FIFO buffer size One channel: Maximum 8 MSa Two channels: Maximum 4 MSa/ch Function generation mode Sine, square, triangle, sawtooth, and noise waveforms Digital I/O TL Number of bits 24-bit programmable input/output Compatibility TL Input voltage V _n = 0.7 V max. I _n = 10 μA max V _n = 2.0 V min. I _n =	Input impedance		1 GΩ / 100 pF		
Trigger sources External analog/digital trigger, SSI/Star trigger? Trigger modes Pre- trigger, delay-trigger, post-trigger, and middle-trigger FIFO buffer size Up to 8 MSa Analog output Resolution 12 bits Number of channels 2 Maximum update rate 1 MSa/s Output ranges 0 to 10 V, ±10 V, 0 to A0_EXT_REF, ±A0_EXT_REF³ Output coupling DC Output impedance 0.1 \(\Omega{ Trigger sources} \) Stability Any passive load up to 1500 pF Power-on state Trigger sources External analog/digital trigger, SSI/Star trigger² Trigger sources External analog/digital trigger, SSI/Star trigger² Trigger modes Post-trigger and delay-trigger One channel: Maximum 8 MSa Two channels: Maximum 8 MSa Two channels: Maximum 4 MSa/ch Function generation mode Sine, square, triangle, sawtooth, and noise waveforms Digital I/O Number of bits 24-bit programmable input/output Compatibility TTL Input voltage \[\begin{array}{cccccccccccccccccccccccccccccccccccc	Operational common mode voltage range		±7.5 V maximum		
Trigger modes	Overvoltage protection	Power-on: C	ontinuous ±30 V, Power-off: (Continuous ±15 V	
FIFO buffer size Analog output Resolution 12 bits Number of channels 2 Maximum update rate 1 MSa/s Output ranges 0 to 10 V, ±10 V, 0 to AO_EXT_REF, ±AO_EXT_REF³ Output coupling DC Output impedance 0.1 Ω Typical Stability Any passive load up to 1500 pF Power-on state 1 V steady state Trigger sources External analog/digital trigger, SSI/Star trigger² Trigger modes Post-trigger and delay-trigger FIFO buffer size One channels: Maximum 8 MSa Two channels: Maximum 4 MSa/ch Function generation mode Sine, square, triangle, sawtooth, and noise waveforms Digital I/O Number of bits Compatibility TTL Input voltage V _{IL} = 0.7 V max, I _{IL} = 10 μA max V _{IL} = 0.7 V max, I _{IL} = 10 μA max V _{IL} = 0.5 V to +5.5 V Output voltage range V _{IL} = 0.45 V max, I _{IL} = 8 mA max	Trigger sources	Externa	ıl analog/digital trigger, SSI/	Star trigger ²	
Analog output Resolution 12 bits Number of channels 2 Maximum update rate 1 MSa/s Output ranges 0 to 10 V, ±10 V, 0 to AO_EXT_REF, ±AO_EXT_REF³ Output coupling DC Output impedance 0.1 Ω Typical Stability Any passive load up to 1500 pF Power-on state 0 V steady state Trigger sources External analog/digital trigger, SSI/Star trigger² Trigger modes Post-trigger and delay-trigger FIFO buffer size One channel: Maximum 8 MSa Two channel: Maximum 4 MSa/ch Function generation mode Sine, square, triangle, sawtooth, and noise waveforms Digital I/O Vinumber of bits Compatibility TTL Input voltage Vinumber of the company of	Trigger modes	Pre- trigger,	delay-trigger, post-trigger, a	nd middle-trigger	
Resolution 12 bits Number of channels 2 Maximum update rate 1 MSa/s Output ranges 0 to 10 V, ±10 V, 0 to AO_EXT_REF, ±AO_EXT_REF³ Output coupling DC Output impedance 0.1 Ω Typical Stability Any passive load up to 1500 pF Power-on state 0 V steady state Trigger sources External analog/digital trigger, SSI/Star trigger² Trigger modes Post-trigger and delay-trigger FIFO buffer size One channel: Maximum 8 MSa Two channels: Maximum 4 MSa/ch Function generation mode Sine, square, triangle, sawtooth, and noise waveforms Digital I/O Number of bits Compatibility TTL Input voltage V _{IL} = 0.7 V max, I _{IL} = 10 μA max V _{IR} = 2.0 V min, I _{IR} = 10 μA max V _{IL} = 0.7 V to +5.5 V Output voltage range -0.5 V to +5.5 V Output voltage range -0.5 V to +5.5 V	FIFO buffer size		Up to 8 MSa		
Number of channels 2 Maximum update rate 1 MSa/s Output ranges 0 to 10 V, ±10 V, 0 to A0_EXT_REF, ±A0_EXT_REF³ Output coupling DC Output impedance 0.1 Ω Typical Stability Any passive load up to 1500 pF Power-on state 0 V steady state Trigger sources External analog/digital trigger, SSI/Star trigger² Trigger modes Post-trigger and delay-trigger FIFO buffer size One channel: Maximum 8 MSa Two channels: Maximum 4 MSa/ch Function generation mode Sine, square, triangle, sawtooth, and noise waveforms Digital I/O Vumber of bits Compatibility TTL Input voltage $V_{IL} = 0.7 \text{ V max}$, $I_{IL} = 10 \text{ µA max}$ V _{II} = 0.7 V min, $I_{IH} = 10 \text{ µA max}$ V _{II} = 0.45 V max, $I_{IL} = 8 \text{ mA max}$ Output voltage range -0.5 V to +5.5 V Output voltage and the voltage range -0.45 V max, $I_{IL} = 8 \text{ mA max}$	Analog output				
Maximum update rate1 MSa/sOutput ranges0 to 10 V, ±10 V, 0 to A0_EXT_REF, ±A0_EXT_REF³Output couplingDCOutput impedance0.1 Ω TypicalStabilityAny passive load up to 1500 pFPower-on state0 V steady stateTrigger sourcesExternal analog/digital trigger, SSI/Star trigger²Trigger modesPost-trigger and delay-triggerFIFO buffer sizeOne channel: Maximum 8 MSa Two channels: Maximum 4 MSa/chFunction generation modeSine, square, triangle, sawtooth, and noise waveformsDigital I/OVal-bit programmable input/outputCompatibilityTTLInput voltage $V_{IL} = 0.7$ V max, $I_{IL} = 10$ μA max $V_{IH} = 2.0$ V min, $I_{IH} = 10$ μA maxInput voltage range -0.5 V to $+5.5$ VOutput voltage range $V_{OI} = 0.45$ V max, $I_{OI} = 8$ mA max	Resolution		12 bits		
Output ranges0 to 10 V, ± 10 V, 0 to AO_EXT_REF, $\pm AO_EXT_REF^3$ Output couplingDCOutput impedance 0.1Ω TypicalStabilityAny passive load up to 1500 pFPower-on state0 V steady stateTrigger sourcesExternal analog/digital trigger, SSI/Star trigger²Trigger modesPost-trigger and delay-triggerFIFO buffer sizeOne channel: Maximum 8 MSa Two channels: Maximum 4 MSa/chFunction generation modeSine, square, triangle, sawtooth, and noise waveformsDigital I/OValue of bitsNumber of bits24-bit programmable input/outputCompatibilityTTLInput voltage $V_{IL} = 0.7 \text{ V max}, I_{IL} = 10 \text{ µA max} V_{IH} = 2.0 \text{ V min}, I_{IH} = 10 \text{ µA max} V_{IH} = 2.0 \text{ V min}, I_{IH} = 10 \text{ µA max} V_{IH} = 2.0 \text{ V min}, I_{IH} = 10 \text{ µA max} V_{IH} = 2.0 \text{ V min}, I_{IH} = 10 \text{ µA max} V_{IH} = 2.0 \text{ V min}, I_{IH} = 10 \text{ µA max} V_{IH} = 2.0 \text{ V min}, I_{IH} = 10 \text{ µA max} V_{IH} = 2.0 \text{ V min}, I_{IH} = 10 \text{ µA max} V_{IH} = 2.0 \text{ V min}, I_{IH} = 2.0 \text{ V min}, I_{IH}$	Number of channels		2		
Output impedance DC Output impedance 0.1Ω Typical Stability Any passive load up to 1500 pF Power-on state 0 V steady state Trigger sources External analog/digital trigger, SSI/Star trigger² Trigger modes Post-trigger and delay-trigger FIFO buffer size One channel: Maximum 8 MSa Two channels: Maximum 4 MSa/ch Function generation mode Sine, square, triangle, sawtooth, and noise waveforms Digital I/O Val-bit programmable input/output Compatibility TTL Input voltage $V_{IL} = 0.7 \text{ V max}$, $I_{IL} = 10 \text{ μA max}$ $V_{IL} = 0.7 \text{ V min}$, $I_{IH} = 10 \text{ μA max}$ $V_{IL} = 0.7 \text{ V min}$, $I_{IH} = 10 \text{ μA max}$ Input voltage range $-0.5 \text{ V to } +5.5 \text{ V}$ Output voltage $V_{IL} = 0.45 \text{ V max}$, $I_{IL} = 8 \text{ mA max}$	Maximum update rate		1 MSa/s		
Output impedance Stability Any passive load up to 1500 pF Power-on state 0 V steady state Trigger sources External analog/digital trigger, SSI/Star trigger² Trigger modes Post-trigger and delay-trigger FIFO buffer size One channel: Maximum 8 MSa Two channels: Maximum 4 MSa/ch Function generation mode Sine, square, triangle, sawtooth, and noise waveforms Digital I/O Volumber of bits Number of bits 24-bit programmable input/output Compatibility TTL Input voltage $V_{IL} = 0.7 \text{ V max}$, $I_{IL} = 10 \text{ μA max}$ $V_{IL} = 0.7 \text{ V min}$, $I_{IH} = 10 \text{ μA max}$ Input voltage range $-0.5 \text{ V to} + 5.5 \text{ V}$ Output voltage $V_{IL} = 0.45 \text{ V max}$, $I_{IL} = 8 \text{ mA max}$	Output ranges	0 to 10 V	±10 V, 0 to AO_EXT_REF, ±	AO_EXT_REF ³	
StabilityAny passive load up to 1500 pFPower-on state0 V steady stateTrigger sourcesExternal analog/digital trigger, SSI/Star trigger²Trigger modesPost-trigger and delay-triggerFIFO buffer sizeOne channel: Maximum 8 MSa Two channels: Maximum 4 MSa/chFunction generation modeSine, square, triangle, sawtooth, and noise waveformsDigital I/OVal-bit programmable input/outputNumber of bits24-bit programmable input/outputCompatibilityTTLInput voltage $V_{IL} = 0.7 \text{ V max}, I_{IL} = 10 \text{ µA max} \text{ V max}$ Input voltage range $-0.5 \text{ V to } +5.5 \text{ V}$ Output voltage $V_{OI} = 0.45 \text{ V max}, I_{OI} = 8 \text{ mA max}$	Output coupling		DC		
Power-on state 0 V steady state Trigger sources External analog/digital trigger, SSI/Star trigger² Trigger modes Post-trigger and delay-trigger FIFO buffer size One channel: Maximum 8 MSa Two channels: Maximum 4 MSa/ch Function generation mode Sine, square, triangle, sawtooth, and noise waveforms Digital I/O Number of bits 24-bit programmable input/output Compatibility TTL Input voltage $V_{IL} = 0.7 \text{ V max}, I_{IL} = 10 \mu\text{A max}$ $V_{IH} = 2.0 \text{ V min}, I_{IH} = 10 \mu\text{A max}$ Input voltage range $-0.5 \text{ V to } +5.5 \text{ V}$ Output voltage.	Output impedance		0.1 Ω Typical		
Trigger sources External analog/digital trigger, SSI/Star trigger² Trigger modes Post-trigger and delay-trigger FIFO buffer size One channel: Maximum 8 MSa Two channels: Maximum 4 MSa/ch Function generation mode Sine, square, triangle, sawtooth, and noise waveforms Digital I/O Number of bits 24-bit programmable input/output Compatibility TTL Input voltage $V_{IL} = 0.7 \text{ V max}, I_{IL} = 10 \mu\text{A max}$ $V_{IH} = 2.0 \text{ V min}, I_{IH} = 10 \mu\text{A max}$ Input voltage range $V_{OI} = 0.45 \text{ V max}, I_{OI} = 8 \text{ mA max}$	Stability		Any passive load up to 1500) pF	
Trigger modes Post-trigger and delay-trigger One channel: Maximum 8 MSa Two channels: Maximum 4 MSa/ch Function generation mode Sine, square, triangle, sawtooth, and noise waveforms Digital I/O Number of bits 24-bit programmable input/output Compatibility TTL Input voltage $V_{IL} = 0.7 \text{ V max}, I_{IL} = 10 \mu\text{A max}$ $V_{IH} = 2.0 \text{ V min}, I_{IH} = 10 \mu\text{A max}$ Input voltage range $V_{OI} = 0.45 \text{ V max}, I_{OI} = 8 \text{ mA max}$	Power-on state		0 V steady state		
FIFO buffer size One channel: Maximum 8 MSa Two channels: Maximum 4 MSa/ch Function generation mode Sine, square, triangle, sawtooth, and noise waveforms Digital I/O Number of bits 24-bit programmable input/output Compatibility TTL Input voltage $V_{IL} = 0.7 \text{ V max}, I_{IL} = 10 \mu\text{A max}$ $V_{IH} = 2.0 \text{ V min}, I_{IH} = 10 \mu\text{A max}$ Input voltage range $V_{OL} = 0.45 \text{ V max}, I_{OL} = 8 \text{ mA max}$	Trigger sources	Externa	ll analog/digital trigger, SSI/	Star trigger ²	
FIFU buffer size Two channels: Maximum 4 MSa/ch Function generation mode Sine, square, triangle, sawtooth, and noise waveforms Digital I/O Number of bits 24-bit programmable input/output Compatibility TTL Input voltage $V_{IL} = 0.7 \text{ V max}, I_{IL} = 10 \mu\text{A max}$ $V_{IH} = 2.0 \text{ V min}, I_{IH} = 10 \mu\text{A max}$ Input voltage range $-0.5 \text{ V to } +5.5 \text{ V}$ Output voltage.	Trigger modes		Post-trigger and delay-trigg	ger	
Digital I/O Number of bits 24-bit programmable input/output Compatibility TTL Input voltage $V_{IL} = 0.7 \text{ V max}, I_{IL} = 10 \text{ µA max}$ Input voltage range $-0.5 \text{ V to } +5.5 \text{ V}$ Output voltage $V_{QI} = 0.45 \text{ V max}, I_{QI} = 8 \text{ mA max}$	FIFO buffer size				
Number of bits 24-bit programmable input/output Compatibility TTL Input voltage $V_{\parallel} = 0.7 \text{ V max, } I_{\parallel} = 10 \mu\text{A max}$ $V_{\parallel} = 2.0 \text{ V min, } I_{\parallel} = 10 \mu\text{A max}$ Input voltage range $-0.5 \text{ V to } +5.5 \text{ V}$ Output voltage $V_{\parallel} = 0.45 \text{ V max, } I_{\parallel} = 8 \text{ mA max}$	Function generation mode	Sine, square, triangle, sawtooth, and noise waveforms			
Compatibility TTL	Digital I/O				
Input voltage $ \begin{aligned} V_{IL} &= 0.7 \text{ V max, } I_{IL} = 10 \mu\text{A max} \\ V_{IH} &= 2.0 \text{ V min, } I_{IH} = 10 \mu\text{A max} \end{aligned} $ Input voltage range $ \begin{aligned} -0.5 \text{ V to } +5.5 \text{ V} \\ V_{QI} &= 0.45 \text{ V max, } I_{QI} = 8 \text{ mA max} \end{aligned} $	Number of bits		24-bit programmable input/o	utput	
$V_{\rm IH} = 2.0 \text{ V min, } I_{\rm IH} = 10 \mu\text{A max}$ Input voltage range $-0.5 \text{ V to } +5.5 \text{ V}$ Output voltage $V_{\rm DI} = 0.45 \text{ V max, } I_{\rm DI} = 8 \text{ mA max}$	Compatibility				
Output voltage $V_{01} = 0.45 \text{ V max}, I_{01} = 8 \text{ mA max}$	Input voltage	$V_{_{ L}}=0.7$ V max, $I_{_{ L}}=10$ μA max $V_{_{ H}}=2.0$ V min, $I_{_{ H}}=10$ μA max			
Output voltage $ \begin{array}{c} V_{_{OL}} = 0.45 \text{ V max, I}_{_{OL}} = 8 \text{ mA max} \\ V_{_{OH}} = 2.4 \text{ V min, I}_{_{OH}} = 400 \mu\text{A max} \\ \end{array} $	Input voltage range		–0.5 V to +5.5 V		
	Output voltage		$V_{OL} = 0.45 \text{ V max}, I_{OL} = 8 \text{ mA}$ $V_{OH} = 2.4 \text{ V min}, I_{OH} = 400 \mu\text{A}$	max max	

High density multifunction USB DAQ (continued))

General purpose digital counter	
Maximum count	(2 ³¹ – 1) bits
Number of channels	Two independent up/down counter
Compatibility	ΠL
Clock source	Internal or external
Base clock available	48 MHz
Maximum clock source frequency	12 MHz
Input frequency range ⁴	0.1 Hz to 6 MHz at 50% duty cycle
Pulse width measurement range	0.167 µs to 178.956 s
Analog trigger	
Trigger source	All analog input channels, External analog trigger (EXTA_TRIG)
Trigger level	±Full scale for internal; ±10 V for external
Trigger conditions	Above high, below low, and window (software selectable)
Trigger level resolution	8 bits
Bandwidth	400 kHz
Input impedance for EXTA_TRIG	20 kΩ
Coupling	DC
Overvoltage protection	Continuous for ±35 V _{maximum}
Digital trigger	
Compatibility	TTL/CMOS
Response	Rising or falling edge
Pulse width	20 ns minimum
Calibration ⁵	
On board reference	5 V
Temperature drift	±2 ppm/°C
Stability	±6 ppm/1000 hrs
General	
Remote interface	Hi-Speed USB 2.0
Device class	USBTMC-USB488
Programmable interface	Standard Commands for Programmable Instruments (SCPI) and IVI-COM



When multiple channels are used in the U2355A or U2356A, the sampling rate of each channel is the maximum sampling rate divided by the number of channels used. For multiple channels used in the U2331A, the sampling rate of each channel = (1 MSa/s) / number of channels used.

² System Synchronous Interface (SSI) and Star trigger commands are used when modular devices are used in the product chassis.

³ Maximum external reference voltage for analog output channels (AO_EXT_REF) is ±10 V.

Measurement frequency's resolution:

^{= 12} MHz/n, n = 2, 3, 4, 5, ..., 120 M

^{= 6} MHz, 4 MHz, 3 MHz, 2.4 MHz, 2.0 MHz, ..., 0.1 Hz (up to six decimal points)

⁵ 20 minutes warm-up time is recommended.

Electrical measurement specifications

Basic multifunction USB DAQ

Model number	U2351A, U2352A		U2353A, U2354A		
Analog input measurement ¹					
Function	23 °C ± 5 °C	0 °C to 18 °C 28 °C to 45 °C	23 °C ± 5 °C	0 °C to 18 °C 28 °C to 45 °C	
Offset error	±1 mV	±5 mV	±1 mV	±5 mV	
Gain error	±2 mV	±5 mV	±2 mV	±5 mV	
–3 dB Small signal bandwidth²	760	kHz	1.5	MHz	
1% THD Large signal bandwidth ²	300	kHz	300	kHz	
System noise	1 mVrms	2 mVrms	1 mVrms	2.5 mVrms	
CMRR	62	dB	62	dB	
Spurious-Free Dynamic Range (SFDR) ³	88	dB	82	dB	
Signal-to-Noise and Distortion Ratio (SINAD) ³	80 dB 78 dB			dB	
Total Harmonic Distortion (THD) ³	-90	dB	−82 dB		
Signal-to-Noise Ratio (SNR) ³	80	dB	78 dB		
Effective Number of Bits (ENOB) ³	1;	3	12.6		
Model number	U2351A, U2353A				
Analog output measurement ¹					
Function	23 °C ± 5 °C		0 °C to 18 °C 28 °C to 45 °C		
Offset error	±1 :	nV	±4	mV	
Gain error	±4r	nV	±5	mV	
Slew rate	19 V/μs				
Rise time	0.9 μs				
Fall time	0.9 μs				
Settling time to 1% output error	4 μs				
Driving capability		Ę	i mA		

 $^{^{1}}$ Specifications are for 20 minutes of warm-up time, calibration temperature at 23 $^{\circ}\text{C}$ and input range of $\pm10\,\text{V}.$

Specifications are based on the following test condition:

Bandwidth test	Model number	Test conditions (DUT setting at $\pm 10~V$ bipolar)	
–3 dB Small signal bandwidth 1% THD large signal bandwidth	U2351A U2352A	Sampling rate: Input voltage: — —3 dB Small signal bandwidth – 1% THD Large signal bandwidth	250 kSa/s - 10% FSR - FSR -1 dB FS
	U2353A U2354A	Sampling rate: Input voltage: — —3 dB Small signal bandwidth — 1% THD Large signal bandwidth	500 kSa/s - 10% FSR - FSR -1 dB FS

5 ns-V (Typical), 80 ns-V (Maximum)

³ Specifications are based on the following test conditions:

Dynamic range test	Model number	Test conditions (DUT setting at ±10 V bipolar)	
SFDR, THD, SINAD, SNR, ENOB	U2351A U2352A	Sampling rate: Fundamental frequency: Number of points: Fundamental input voltage:	250 kSa/s 2.4109 kHz 8192 FSR –1 dB FS
	U2353A U2354A	Sampling rate: Fundamental frequency: Number of points: Fundamental input voltage:	500 kSa/s 4.974 kHz 16384 FSR –1 dB FS

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Glitch energy

High density multifunction USB DAQ

Model number	U2355A		U2356A		U2331A	
Analog input measurement ¹						
Function	23 °C ± 5 °C	0 °C to 18 °C 28 °C to 45 °C	23 °C ± 5 °C	0 °C to 18 °C 28 °C to 45 °C	23 °C ± 5 °C	0 °C to 18 °C 28 °C to 45 °C
Offset error	±1 mV	±2 mV	±1 mV	±2 mV	±2 mV	±3 mV
Gain error	±2 mV	±3 mV	±2 mV	±6 mV	±6 mV	±7.5 mV
-3 dB small signal bandwidth ²	760	kHz	1.3 MHz		1.2 MHz	
1% THD large signal bandwidth ²	400	kHz	400 kHz		N/A	
System noise	1 mVrms	2 mVrms	1 mVrms	4 mVrms	3 mVrms	5 mVrms
CMRR	64	dB	61 dB		62	dB
Spurious-Free Dynamic Range (SFDR) ³	88	dB	86 dB		71 dB	
Signal-to-Noise and Distortion Ratio (SINAD) ³	80 dB		78 dB		72 dB	
Total Harmonic Distortion (THD) ³	−90 dB		−84 dB		–76 dB	
Signal-to-Noise Ratio (SNR) ³	80 dB		78 dB		72 dB	
Effective Number of Bits (ENOB) ³	1	3	12	2.6	1	1.6

 $^{^{1}}$ Specifications are for 20 minutes of warm-up time, calibration temperature at 23 °C and input range of ±10 V.

² Specifications are based on the following test conditions.

Bandwidth test	Model number	Test conditions (DUT setting at ±10 V bipolar)		
-3 dB Small signal bandwidth 1% THD Large signal bandwidth	U2355A	Sampling rate: Input voltage:	250 kSa/s	
170 TTD Earge Signal Ballawidth		 – 3 dB Small signal bandwidth 	10% FSR	
		 1% THD Large signal bandwidth 	FSR –1 dB FS	
	U2356A	Sampling rate: Input voltage:	500 kSa/s	
		3 dB Small signal bandwidth	10% FSR	
		 1% THD Large signal bandwidth 	FSR –1 dB FS	
	U2331A	Sampling rate: Input voltage:	3 MSa/s	
		3 dB Small signal bandwidth	10% FSR	
		 1% THD Large signal bandwidth 	FSR –1 dB FS	

 $^{^{\}rm 3}$ $\,$ Specifications are based on the following test conditions.

Dynamic range test	Model number	Test conditions (DUT setting at ±10 V bipolar)		
–3 dB Small signal bandwidth 1% THD Large signal bandwidth	U2355A	Sampling rate: Fundamental frequency: Number of points: Fundamental input voltage:	250 kSa/s 2.4109 kHz 8192 FSR –1 dB FS	
	U2356A	 Sampling rate: Fundamental frequency: Number of points: Fundamental input voltage: 	500 kSa/s 4.974 kHz 16384 FSR -1 dB FS	
	U2331A	Sampling rate: Fundamental frequency: Number of points: Fundamental input voltage:	3 MSa/s 29.892 kHz 65536 FSR -1 dB FS	

Datasheet

High density multifunction USB DAQ (continued)

Model number	U2355A	, U2356A	U2331A	
Analog output measurement ¹ Function	23 °C ± 5 °C	0 °C to 18 °C 28 °C to 45 °C	23 °C ± 5 °C	0 °C to 18 °C 28 °C to 45 °C
Offset error	±1 mV	±4 mV	±1.5 mV	±3 mV
Gain error	±4 mV	±5 mV	±4 mV	±5 mV
Slew rate	19 '	V/µs	19 \	V/µs
Rise time	0.0) μs	0.0) µs
Fall time	0.0) μs	0.0) µs
Settling time to 1% output error	4	μs	4	μs
Driving capability	5	mA	5	mA
Glitch energy	5 ns-V (Typical), 8	0 ns-V (Maximum)	5 ns-V (Typical), 8	0 ns-V (Maximum)



 $^{^{1}}$ Specifications are for 20 minutes of warm-up time, calibration temperature at 23 $^{\circ}\text{C}$ and input range of $\pm10\,\text{V}.$

DC Characteristics

Accuracy specifications

Model Number		U2351A, U2352A, U Analog Input	2353A, U2354A
Unipolar Range (V)	Offset Error (mV) ¹	Gain Error (mV)	Accuracy (% of reading + offset error) ²
10	1.5	2.0	0.04% + 1.5 mV
5	1.5	2.0	0.08% + 1.5 mV
2.5	1.0	1.0	0.08% + 1.0 mV
1.25	1.0	1.0	0.16% + 1.0 mV
Bipolar Range (V)	Offset Error (mV) ¹	Gain Error (mV)	Accuracy (% of reading + offset error) ²
10	1.0	2.0	0.02% + 1.0 mV
5	1.0	2.0	0.04% + 1.0 mV
2.5	1.0	1.5	0.06% + 1.0 mV
1.25	1.0	1.5	0.12% + 1.0 mV
Model Number		U2355A, U	2356A
Unipolar Range (V)	Offset Error (mV) ¹	Gain Error (mV)	Accuracy (% of reading + offset error) ²
10	1.0	1.5	0.03% + 1.0 mV
5	1.0	1.5	0.06% + 1.0 mV
2.5	1.0	1.0	0.08% + 1.0 mV
1.25	1.0	1.0	0.16% + 1.0 mV
Bipolar Range (V)	Offset Error (mV) ¹	Gain Error (mV)	Accuracy (% of reading + offset error) ²
10	1.0	2.0	0.02% + 1.0 mV
5	1.0	2.0	0.04% + 1.0 mV
2.5	1.0	1.5	0.06% + 1.0 mV
1.25	1.0	1.5	0.12% + 1.0 mV
Model		U2331	IA
Jnipolar Range (V)	Offset Error (mV) ¹	Gain Error (mV)	Accuracy (% of reading + offset error) ²
10	1.5	4.0	0.08% + 1.5 mV
5	1.5	2.0	0.08% + 1.5 mV
4	1.5	2.0	0.10% + 1.5 mV
2.5	1.0	1.5	0.12% + 1.0 mV
2	1.0	1.0	0.10% + 1.0 mV
1	1.0	1.0	0.20% + 1.0 mV
0.5	1.0	1.0	0.41% + 1.0 mV
0.4	1.0	1.0	0.51% + 1.0 mV
0.1	1.0	1.0	2.04% + 1.0 mV

⁻ The above specifications are typical for 23 °C.



Specifications are for 20 minutes warm-up and self calibration.

The measurement are calculated with 100 points avearaging of data.

¹ Offset error is measured at midscale of full range.

 $^{^2}$ $\;$ Accuracy = \pm [% of | (Gain error / (Measured value – Midscale of FSR)) | + Offset error]

Accuracy specifications (continued)

Model	U2331A				
Bipolar Range (V)	Offset Error (mV) ¹	Gain Error (mV)	Accuracy (% of reading + offset error) ²		
10	2.0	6.0	0.06% + 2.0 mV		
5	1.5	4.0	0.08% + 1.5 mV		
2.5	1.5	2.0	0.08% + 1.5 mV		
1.25	1.0	1.5	0.12% + 1.0 mV		
1	1.0	1.0	0.10% + 1.0 mV		
0.5	1.0	1.0	0.20% + 1.0 mV		
0.25	1.0	1.0	0.40% + 1.0 mV		
0.2	1.0	1.0	0.50% + 1.0 mV		
0.05	1.0	1.0	2.02% + 1.0 mV		

- The above specifications are typical for 23 °C.
- Specifications are for 20 minutes warm-up and self calibration.
- The measurement are calculated with 100 points averaging of data.

Model		U2351A, U2352A, U	12353A, U2354A
		Analog output	
Unipolar Range (V)	Offset Error (mV) ³	Gain Error (mV)	Accuracy (% of reading + offset error) ⁴
10	1.0	2.0	0.02% + 1.0 mV
Bipolar Range (V)	Offset Error (mV) ³	Gain Error (mV)	Accuracy (% of reading + offset error) ⁴
10	1.0	4.0	0.04% + 1.0 mV
Model		U2355A, U	12356A
Unipolar Range (V)	Offset Error (mV) ³	Gain Error (mV)	Accuracy (% of reading + offset error) ⁴
10	1.0	2.0	0.02% + 1.0 mV
Bipolar Range (V)	Offset Error (mV) ³	Gain Error (mV)	Accuracy (% of reading + offset error) ⁴
10	1.0	4.0	0.04% + 1.0 mV
Model		U233	1A
Unipolar Range (V)	Offset Error (mV) ³	Gain Error (mV)	Accuracy (% of reading + offset error) ⁴
10	2.5	4.0	0.04% + 2.5 mV
Bipolar Range (V)	Offset Error (mV) ³	Gain Error (mV)	Accuracy (% of reading + offset error) ⁴
10	1.5	4.0	0.04% + 1.5 mV

- $-\,$ The above specifications are typical for 23 °C.
- Specifications are for 20 minutes warm-up and self calibration.



¹ Offset error is measured at midscale of full range.

² Accuracy = \pm [% of | (Gain error / (Measured value – Midscale of FSR)) | + Offset error]

Offset error is measured at 0 V.

⁴ Accuracy = \pm [% of | Gain error/Output value | + Offset voltage]

Keysight Measurement Manager

The Keysight Measurement Manager (AMM) is an application data viewer software that comes with the standard purchase of the U2300A Series USB modular data acquisition. This software is designed to help you perform quick device configuration, data logging and data acquisition using the products.

Supported features found in the U2300A Series USB modular multifunction data acquisition devices:

- Averaging
- Command logger
- Self-calibration
- Option to save the current instrument configuration to a file
- Data logging and export feature to CSV, HTML and text only format files that can be printed
- Data viewer to load and review previously logged data
- Trigger settings between modules in the instrument chassis with Star trigger and Master/Slave trigger
- Synchronization display and data logging for modules in the instrument chassis

Keysight Measurement Manager prerequisites

Prior to installing the Keysight Measurement Manager software, ensure that your PC meets the following minimum system requirements for installation and operations.

Requirement	Windows XP operating systems	Windows Vista operating systems	Windows 7 operating systems
Operating system	Windows XP Service Pack 3 (or later) ¹	Windows Vista (32-bit) Service Pack 1 and 2 ²	Windows 7 (32-bit and 64-bit) ^{3, 4}
Processor speed	600 MHz or higher required, 800 MHz recommended	1 GHz 32-bit (x86)	3 GHz 32-bit (x86)
Memory	256 MB minimum (1 GB or greater recommended)	1 GB minimum	2 GB minimum
Hard-disk space	1.5 GB minimum	1.5 GB minimum	1.5 GB minimum
Video	Super VGA (800 × 600) 256 colors or more	Support for DirectX 9 graphics with 128 MB graphics memory recommended ⁵	Support for DirectX 9 graphics with 128 MB graphics memory recommended ⁵
CD-ROM drive or DVD-ROM drive ⁶	Required	Required	Required
Browser	Microsoft Internet Explorer 5.01 or greater	Microsoft Internet Explorer 7 or greater	Microsoft Internet Explorer 7 or greater

- ¹ Supported Windows XP editions Home or Professional
- Supported Windows Vista (32-bit) editions Home Basic, Home Premium, Business, or Ultimate
- ³ Supported Windows 7 (32-bit and 64-bit) editions Home Basic, Home Premium, Professional, Enterprise, or Ultimate
- ⁴ Keysight Measurement Manger for Windows 7 64-bit support is a 32-bit application running on a WOW64 (Windows-on-Windows 64-bit) emulator.
- ⁵ Super VGA graphics is supported for Windows Vista and Windows 7.
- The type of media provided with the product determines whether a CD-ROM drive or DVD-ROM drive is required.

Software requirements

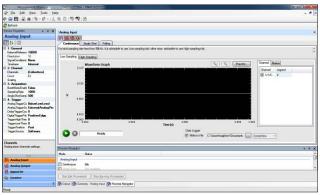
Keysight IO Libraries Suite 15.1 and above¹

Keysight T&M Toolkit Runtime version 2.12

Keysight T&M Toolkit Redistributable Package 2.1 patch²

Microsoft .NET Framework version 2.02

- 1 Available on the Keysight Automation-Ready CD-ROM.
- Bundled with Keysight Measurement Manager software application installer.



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Other products in the Keysight USB Modular Data Acquisition (DAQ) Family



U2500A Series USB Modular Simultaneous Sampling Multifunction DAQ

Features:

- High analog input sampling rate coverage of up to 2 MSa/s for a each channel
- High speed USB 2.0
- Simultaneous acquisition of multiple data points
- Multifunction capabilities analog input (AI), analog output (AO), digital input output (DIO), and counter

For more information: www.keysight.com/find/U2500A



U2600A Series USB Modular Isolated Digital I/O

Features:

- 64 opto-isolated lines that can meet demand up to 24 V
- High speed USB 2.0
- Isolation voltage of 1250 Vrms for protection from transient voltage spikes

For more information: www.keysight.com/find/U2600A



U2781A USB Modular Product Chassis

Features:

- Expansion of channels for each modular product
- Multiple instrument synchronization
- Internal and external 10 MHz reference clock
- High-speed USB 2.0
- SSI/Star trigger bus synchronization between external trigger source and modules

For more information: www.keysight.com/find/U2781A



Optional Accessories

The Keysight U2802A is a 31-channel thermocouple signal conditioning module with a built-in thermistor for cold junction compensation. The U2802A is designed to convert low input voltage signals (less than ±100 mV) from a thermocouple into an output voltage range suitable for data acquisition devices (±10 V). The U2802A device is to be used in conjunction with the Keysight U2355A or U2356A model DAQ device to enable temperature measurements using thermocouples. It works as a standalone device attached to a single DAQ device via two SCSI-II 68 conductor cables. Designed with the compatibility to complement eight standard thermocouple types, it caters to a wide range of applications in various industrial settings.

Features to meet your demands

- 31 input channels that can be independently configured to either differential thermocouple input mode, single-ended voltage input mode, or differential voltage input mode using two input channels set to voltage input mode
- Supports the standard thermocouple types (J, K, R, S, T, N, E, and B) defined in the NIST ITS-90 Thermocouple Database
- Error detection for open thermocouple channels
- Built-in isothermal construction on terminal block for improved measurement accuracy
- Built-in thermistor for cold junction compensation
- Built-in zeroing function to compensate for overall system offset errors due to temperature drift and long term drift
- Up to ±10 V input voltage range for higher voltage inputs
- Sampling rate of 500 kSa/s for overall module
- Sampling rate of 10 kSa/s total for all channels in thermocouple mode
- Quick and easy USB setup
- Robust, cost-effective, and user friendly

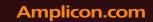
Applications

The U2802A thermocouple input device is designed for robust and demanding industrial applications. This product is suitable and ideal for thermocouple measurement applications such as;

- Product thermal analysis and characterization
- Environmental chamber profiling
- Process monitoring in consumer electronics markets
- Material properties testing in education environments
- Study of electronic temperature properties
- Appliances testing



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IT and Instrumentation for industry



Thermocouple input mode

In thermocouple input mode, the U2802A can acquire up to ±100 mV input signals. Each channel includes an instrumentation amplifier and a 4 Hz low-pass filter. The low-pass filter removes unwanted noise from the thermocouple wires to obtain accurate measurement data.

Voltage input mode

Alternatively, you can select separate voltage input modes for each channel. The channel will be set to bypass the amplifier and filter, allowing up to ±10V input signals to be directly routed to the DAQ device analog input. The bandwidth in this mode is more than 500 kHz.

Zero mode

In zero mode, the positive and negative inputs of the instrumentation amplifier are shorted together. The voltage measured in this mode corresponds to the offset voltage of the channel. You can subtract this offset voltage from subsequent thermocouple mode measurements to increase measurement accuracy. This mode is only applicable in thermocouple mode.

Thermocouple compatibility

The U2802A is compatible with a wide range of standard thermocouple types defined in the NIST ITS-90 Thermocouple Database. This includes types J, K, R, S, T, N, E, and B.

Open thermocouple detection

The U2802A includes open thermocouple detection circuitry to indicate the presence of an open thermocouple.

Calibration EEPROM

The U2802A gain and offset calibration factors for each channel are stored in the EEPROM during factory calibration and can be retrieved prior to taking measurements. This on-board EEPROM also stores the module ID, serial number, and date of calibration for your reference. A section of the EEPROM is also allocated for you to save your calibration data.

Restoring factory calibration

Using the AMM software, you can easily restore the U2802A calibration data from your settings to the original factory settings.



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Amplicon.com

IT and Instrumentation for industry



Product outlook and dimensions

Front view



Rear view



Top and side view



Standard shipped accessories

- Power supply splitter
- Two 68-pin SCSI cables (1 m)
- One J-type thermocouple
- Keysight U2802A 31-Channel Thermocouple Input Device Quick Start Guide
- Keysight USB Modular Products Reference CD-ROM
- Keysight Automation-Ready CD-ROM (contains the Keysight IO Libraries Suite)
- Certificate of Calibration

Product characteristics and general specifications

Power consumption

+12 VDC, 480 mA maximum

Operating environment

- Operating temperature from 0 °C to +55 °C
- Relative humidity at 50% to 85% RH (non-condensing)
- Altitude up to 2000 meters

Storage compliance

–40 °C to 70 °C

Safety compliance

Certified with:

- IEC 61010-1:2001/EN 61010-1:2001 (2nd Edition)

EMC compliance

- IEC 61326-1:2002/EN 61326-1:1997+A2:2001+A3:2003
- CISPR 11: 1990/EN 55011:1990-Group 1 Class A
- Canada: ICES-001:2004
- Australia/New Zealand: AS/NZS CISPR 11:2004

Shock and vibration

Tested to IEC/EN 60068-2

10 connector

- 2 × 68-pin female SCSI connector
- 2 × 34 pin screw terminal block
- 1 × 24 pin screw terminal block

Dimension (W \times D \times H)

158.70 mm × 254.20 mm × 40.50 mm

Weight

1.036 kg

Warranty

Three years for U2802A

Three months for standard shipped accessories



Product specifications

General characteristics	
Number of channels	31 differential and 1 CJC
Input voltage range for voltage mode	±10 V (signal + common mode)
Input voltage (thermocouple mode)	±100 mV
Sampling rate for thermocouple mode	10 kSa/s total for all channels
Sampling rate for overall module	500 kSa/s
Thermocouple types	J, K, R, S, T, N, E, and B
Input specifications	
Accuracy (thermocouple mode) Overall gain error Overall offset error Nonlinearity	0.06% (23 °C \pm 5 °C) 15 μ V (without zeroing) (23 °C \pm 5 °C) 6 μ V (with zeroing) < 0.005% of full scale range
System noise (rms) — Gain (× 1) — Gain (× 100)	100 μVrms 5 μVrms
Common Mode Rejection Ratio (CMRR) - Voltage mode - Thermocouple mode	> 60 dB > 80 dB
Cold junction accuracy	± 1.0 °C typical (23 °C \pm 5 °C) ± 1.5 °C typical (0 °C to 18 °C, 28 °C to 55 °C)
Input characteristics	
Bandwidth (voltage mode)	> 500 kHz
Bandwidth (thermocouple mode)	4.0 Hz
Overvoltage protection ¹	TC Mode ² — Common mode: ±17 V (TC+ and TC- with respect to GND) — Differential mode: ±7 V (Differential voltage between TC+ and TC-) Bypass mode ±20 V (TC+ input with respect to GND) Power-off Mode ±11 V (TC+, TC- input with respect to GND)
Input impedance	> 1 GΩ
Input bias current	±2.5 nA max
Input offset current	±1.5 nA max
Gain drift	60 ppm/°C max
Offset drift	1 μV/°C max
Filter cutoff frequency (–3 dB) (thermocouple mode)	4.0 Hz
Filter type (thermocouple mode)	Low Pass RC Filter
Filter type (thermocouple mode) Other features	Low Pass RC Filter

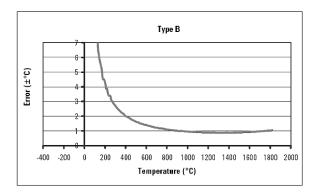
On the channels configured for thermocouple mode, the TC+ and TC- pins can tolerate up to ±17 V of differential voltage for a few minutes. However, exceeding a voltage range of ±100 mV on these channels can cause additional current to be drawn from the device's power supply regulators, which may damage the device if multiple channels are overdriven for prolonged periods. This is the case when a voltage source is tied across the TC_n+ and TC_n- pin. Voltage sources greater than ±100 mV should be tied to TC_n+ and GND (floating source), or TC_n+ and TC_{n-1}+ (grounded source), and have the channels set for bypass mode.

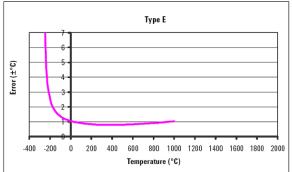


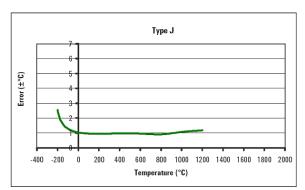
¹ The overvoltage protection levels specified above indicate the maximum voltage each input pin can tolerate without resulting in any damages. However, prolonged exposure to these levels may affect device safety and reliability. Hence, it should be avoided where possible.

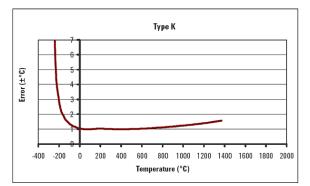
Thermocouples typical measurement accuracy

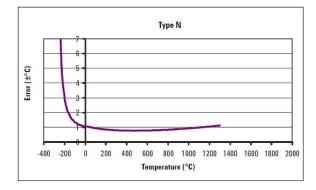
The U2802A measurement error with U2355A or U2356A at 23 °C \pm 5 °C is shown below.

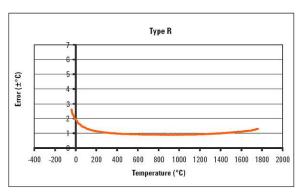


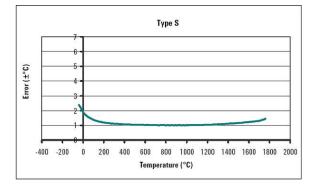


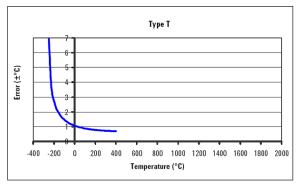












System accuracy specifications

The U2802A system accuracy specifications are shown in Table 1, Table 2, and Table 3. These measurements are derived from the U2802A and DAQ input accuracy specifications without including the thermocouple error. Refer to "Calculating System Accuracy" section in the Keysight U2802A 31-Channel Thermocouple Input Device User's Guide for calculation methodology.

Table 1. Measurement accuracy of the U2355A and U2356A at 23 $^{\circ}$ C \pm 5 $^{\circ}$ C

	Th	nermocouple meası	ırement accura	cy (U2355A, U23	356A at 23 °C ± 5 °	C)	
T/C type	ITS-90 Temper	ature range (°C)	Optimum measurement range(°C)		Without averaging	50 points averaging	500 points averaging
	Low	High	Low	High	(± °C)	(± °C)	(± °C)
В	0	1820	1100	1820	1.9	1.2	1.0
			400	1100	4.4	2.5	2.0
Е	-270	1000	-150	1000	1.7	1.6	1.6
			-200	-150	2.4	2.3	2.3
J	-210	1200	-150	1200	1.6	1.5	1.5
			-210	-150	2.7	2.6	2.5
K	-270	1372	-100	1200	1.5	1.4	1.4
			-200	-100	2.7	2.6	2.6
N	-270	1300	-100	1300	1.5	1.3	1.3
			-200	-100	3.0	2.7	2.6
R	-50	1768	300	1760	2.0	1.4	1.3
			– 50	300	5.0	3.1	2.6
S	-50	1768	400	1760	2.1	1.6	1.4
			-50	400	4.5	2.8	2.4
T	-270	400	-100	400	1.5	1.4	1.4
			-200	-100	2.7	2.5	2.5

Table 2. Measurement accuracy of the U2355A at 0 to 18 °C and 28 to 45 °C

	Ther	mocouple measu	rement accuracy	(U2355A at 0 to 1	8 °C and 28 to 4	5 °C)	
T/C type	ITS-90 Temper	ature range (°C)	-	Optimum measurement range (°C)		50 points averaging	500 points averaging
	Low	High	Low	High	(± °C)	(± °C)	(± °C)
В	0	1820	1100	1820	3.4	2.4	2.2
			400	1100	7.5	3.6	2.2
E	-270	1000	-150	1000	2.7	2.6	2.5
			-200	-150	3.8	3.6	3.6
J	-210	1200	-150	1200	2.5	2.4	2.4
			-210	-150	4.2	4.0	3.9
K	-270	1372	-100	1200	2.9	2.8	2.8
			-200	-100	4.3	4.0	3.9
N	-270	1300	-100	1300	2.6	2.5	2.5
			-200	-100	4.9	4.2	4.0
R	-50	1768	300	1760	3.8	3.1	3.0
			-50	300	8.5	4.6	3.3
S	-50	1768	400	1760	4.2	3.4	3.2
			-50	400	7.7	4.2	3.1
T	-270	400	-100	400	2.4	2.2	2.2
			-200	-100	4.3	4.3	3.9

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Table 3. Measurement accuracy of the U2356A at 0 to 18 $^{\circ}\text{C}$ and 28 to 45 $^{\circ}\text{C}$

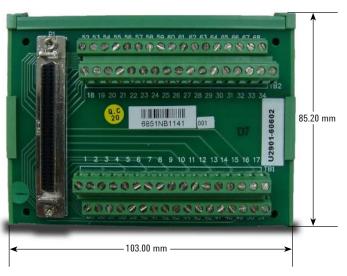
	Ther	mocouple measu	rement accuracy	(U2356A @ 0 to 1	8 °C and 28 to 4	5 °C)	
T/C type	ITS-90 Temperature range (°C)		Optimum measurement range (°C)		Without averaging	50 points averaging	500 points averaging
	Low	High	Low	High	(± °C)	(± °C)	(± °C)
В	0	1820	1100	1820	6.1	3.1	2.4
			400	1100	14.4	6.3	2.7
Е	-270	1000	-150	1000	3.0	2.6	2.6
			-200	-150	4.2	3.7	3.6
J	-210	1200	-150	1200	2.9	2.5	2.5
			-210	-150	4.9	4.1	4.0
K	-270	1372	-100	1200	3.3	2.9	2.9
			-200	-100	5.3	4.2	4.0
N	-270	1300	-100	1300	3.4	2.7	2.6
			-200	-100	6.8	4.6	4.1
R	– 50	1768	300	1760	6.2	3.7	3.2
			-50	300	15.7	7.2	3.8
S	– 50	1768	400	1760	6.4	4.0	3.4
			-50	400	14.2	6.6	3.4
T	-270	400	-100	400	3.0	2.4	2.2
			-200	-100	5.3	4.2	3.9

U2901/U2902A -Terminal block and SCSI- II 68-pin connector with 1-meter/2-meter cable

The U2901A/U2902A is a terminal block and SCSI-II 68-pin connector with 1 meter cable or 2 meter cable that can be used conjunction with the U2300A Series and U2500A Series.

Terminal block overview

Front view





Ordering information

Model	Description
U2351A	16-Channel 250kSa/s USB modular multifunction DAQ
U2352A	16-Channel 250kSa/s USB modular multifunction DAQ; without analog output
U2353A	16-Channel 500kSa/s USB modular multifunction DAQ
U2354A	16-Channel 500kSa/s USB modular multifunction DAQ; without analog output
U2355A	64-Channel 250kSa/s USB modular multifunction DAQ
U2356A	64-Channel 500kSa/s USB modular multifunction DAQ
U2331A	64-Channel 1MSa/s USB modular multifunction DAQ

Optional accessories

Model	Description
U2802A	U2802A 31-Channel Thermocouple Input Device
U2901A	Terminal block and SCSI-II 68-pin connector with 1-meter cable
U2902A	Terminal block and SCSI-II 68-pin connector with 2-meter cable