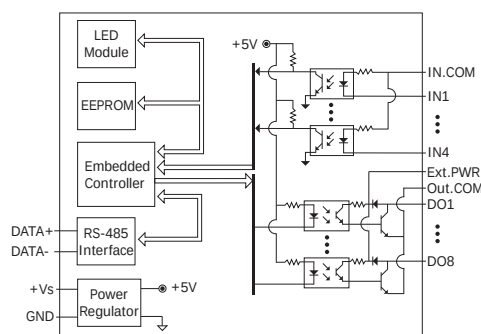




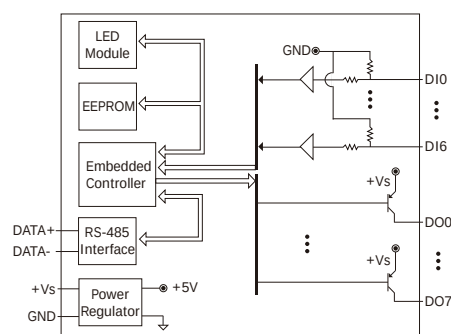
Digital Input & Output Module						
Model Name		I-7044(D)	I-7050(D)	I-7050A(D)	I-7055(D)	I-7055(D)-NPN
			M-7050(D)		M-7055(D)	M-7055(D)-NPN
Pictures						
Digital Input						
Channel		4	7		8	
Contact		Wet	Dry	Wet	Dry + Wet	
Sink/Source (NPN/PNP)		Sink/Source	Source	Sink	Dry: Source Wet: Sink/Source	
Wet Contact	On Voltage Level	+1 V _b c Max.	-	+4 ~ 30 V _b c	+10 ~ 50 V _b c	
	Off Voltage Level	+4 ~ 30 V _b c	-	+1 V _b c Max.	+4 V _b c Max.	
Dry Contact	On Voltage Level	-	Open	-	Close to GND	
	Off Voltage Level	-	Close to GND	-	Open	
Counter (100 Hz, 16-bit)		Yes	Yes		Yes	
Input Impedance		3 K Ω	100 K Ω		10 K Ω	
Overvoltage Protection		+/-35 V _b c	-		+/-70 V _b c	
Digital Output						
Channel		8				
Type	Open Collector	Open Collector	Open Emitter	MOSFET		
Sink/Source (NPN/PNP)	Sink	Sink	Source	Source	Sink	
Load Voltage	+3.5 ~ 30 V _b c		+3.5 ~ 30 V _b c		+10 ~ 40 V _b c	+3.5 ~ 50 V _b c
Max. Load Current	375 mA/Channel		30 mA/Channel		650 mA/Channel	700 mA/Channel
Overvoltage Protection	-		-		47 V _b c	60 V _b c
Overload Protection			-		1.4 A (with short-circuit protection)	
Power-on Value	Yes					
Safe Value	Yes					
System						
Dual Watchdog	Yes					
ESD (IEC 61000-4-2)	+/-2 kV	-			+/-4 kV	
EFT (IEC 61000-4-4)	+/-2 kV	-			+/-4 kV	
Surge (IEC 61000-4-5)			-		-	+/-3 kV
Intra-Module Isolation, Field-to-Logic	3750 V _{rms}	-			3750 V _{rms}	
Power Input	10 ~ 30 V _b c					
Power Consumption	1.0 W; 1.7 W for (D) version	0.4 W; 1.1 W for (D) version	0.5 W; 1.2 W for (D) version	0.8 W; 1.6 W for (D) version	1.2 W; 2.2 W for (D) version	

Internal I/O Structure

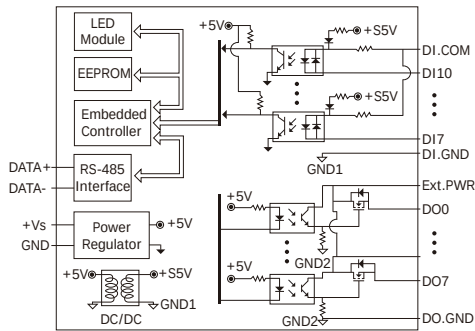
I-7044(D)



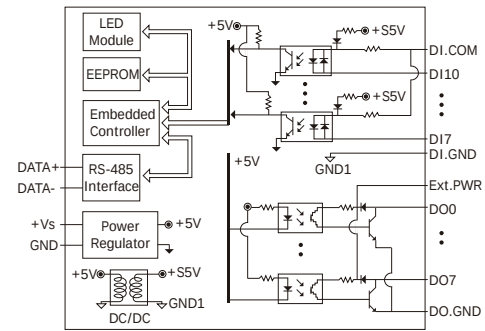
I-7050(D)/I-7050A(D)M-7050(D)



I-7055(D)/M-7055(D)

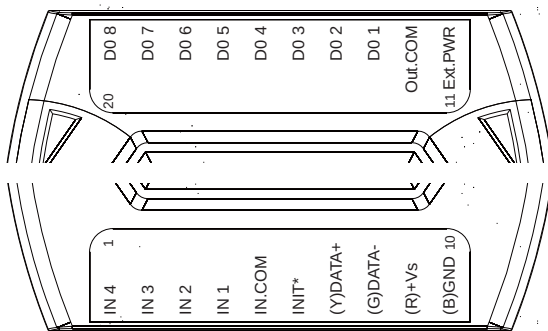


I-7055(D)-NPN/M-7055(D)-NPN

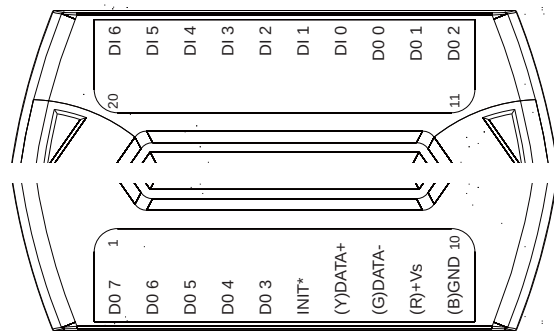


Pin Assignments

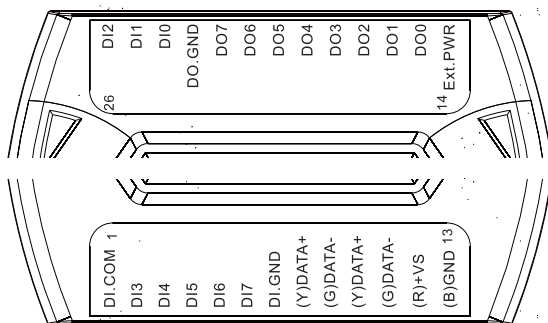
I-7044(D)



I-7050(D)/I-7050A(D)M-7050(D)



I-7055(D)/M-7055(D)



I-7055(D)-NPN/M-7055(D)-NPN

